

## TITLE OF THE INVENTION

## High Frequency Circuit

## BACKGROUND OF THE INVENTION

## 5 Field of the Invention

The present invention relates to a high frequency circuit including a transistor.

## Description of the Background Art

10 Conventionally, various high frequency circuits such as amplifiers using active devices such as bipolar transistors or field effect transmitters have been used. Fig. 18 is a circuit diagram of a conventional emitter grounded wide-band amplifier using a bipolar transistor.

15 The amplifier shown in Fig. 18 comprises a bipolar transistor (hereinafter abbreviated as a transistor) 200, resistors 11, 12, 13, 14, and 15, and capacitors 16, 17, and 18. The transistor 200 has its base connected to a node N11. The node N11 is connected to an input terminal NI through the  
20 capacitor 16. The resistor 11 is connected between a power supply terminal receiving a power supply voltage  $V_{cc}$  and the node N11, and the resistor 12 is connected between the node N11 and a grounding terminal.

The transistor 200 has its collector connected to the  
25 power supply terminal through the resistor 13, and is

connected to an output terminal NO through the capacitor 17. The transistor 200 has its emitter connected to the grounding terminal through the resistor 14, and is connected to the grounding terminal through the resistor 15 and the capacitor 18. An input signal is fed to the input terminal NI, and an amplified output signal is outputted from the output terminal NO.

A DC bias point of the transistor 200 shown in Fig. 18 is determined by a resistance value  $R_1$  of the resistor 11, a resistance value  $R_2$  of the resistor 12, a resistance value  $R_c$  of the resistor 13, a resistance value  $R_e$  of the resistor 14, and the power supply voltage  $V_{cc}$ . As the DC bias point of the transistor 200, a base voltage  $V_b$ , an emitter voltage  $V_e$ , a collector voltage  $V_c$ , and a collector current  $I_c$  are considered. First, the base voltage  $V_b$  is expressed by the following equation:

$$V_b = R_2 \cdot V_{cc} / (R_1 + R_2) \quad \cdots (1)$$

The emitter voltage  $V_e$  is expressed by the following equation:

$$V_e = V_b - V_{BE} \quad \cdots (2)$$

where  $V_{BE}$  is a voltage between the base and the emitter. The voltage  $V_{BE}$  between the base and the emitter is generally constant, for example, approximately 0.6 to 0.7 V. Further, an emitter current  $I_e$  is expressed by the following equation:

$$I_e = V_e / R_e \quad \cdots (3)$$

A base current  $I_B$  becomes a value significantly smaller than the collector current  $I_C$  and the emitter current  $I_E$ , and is generally approximately one-hundredth the collector current  $I_C$  and the emitter current  $I_E$ . Therefore,  $I_E \approx I_C$  in an approximate manner. Consequently, the following equation (4) holds:

$$V_C = V_{CC} - I_C \cdot R_C \quad \cdots (4)$$

In the amplifier shown in Fig. 18, if it is assumed that  $V_{CC} = 15$  [V],  $V_{BE} = 0.6$  [V],  $R_1 = 100$  [k $\Omega$ ],  $R_2 = 12$  [k $\Omega$ ],  $R_E = 1$  [k $\Omega$ ], and  $R_C = 10$  [k $\Omega$ ], the DC bias point of the transistor 200 is  $V_B = 1.6$  [V],  $V_E = 1.0$  [V],  $I_E = I_C = 1.0$  [mA], and  $V_C = 5$  [V].

Then consider voltage gain in the amplifier shown in Fig. 18. If the capacitance value  $C_E$  of the capacitor 18 inserted on the side of the emitter of the transistor 200 is large, and its impedance is sufficiently small, voltage gain  $A_v$  in a low frequency region of the amplifier is expressed by the following equation:

$$A_v = R_C / R_x \quad \cdots (5)$$

where  $R_x$  is a composite resistance value of the resistors 14 and 15 which are connected in parallel, and is expressed by the following equation:

$$R_x = R_E \cdot R_{EE} / (R_E + R_{EE})$$

The voltage gain  $A_v$  in the low frequency region of the amplifier is determined by the ratio of the resistance value  $R_C$  of the resistor 13 to the composite resistance value  $R_x$  of

the resistors 14 and 15 which are connected in parallel, and does not depend on the DC bias point. In the above-mentioned bias conditions, the voltage gain  $A_v$  in direct current is 10. If the resistance value  $R_{EE}$  of the resistor 15 is taken as 1 k $\Omega$ , for example, however, the voltage gain  $A_v$  in the low frequency region is 20.

In order to prevent the voltage gain  $A_v$  in the low frequency region from being reduced in the above-mentioned amplifier, however, the capacitor 18 having a large capacitance value  $C_E$  must be used. In a case where the amplifier shown in Fig. 18 is constituted by an integrated circuit, therefore, when a capacitor having an MIM (Metal-Insulator-Metal) structure having a relatively large capacitance value per unit area is used as the capacitor 18, the occupied area thereof is increased. Accordingly, the amplifier cannot be miniaturized.

In the actual amplifier, a mirror effect is produced. Accordingly, the gain thereof in a high frequency region is reduced. Here, the mirror effect is such a phenomenon that when a capacitor is connected to an input terminal and an output terminal of the amplifier, a case where the capacitance value of the capacitor is small is equivalent to a case where a capacitor having a large capacitance value is connected, as viewed from the input side.

Generally in the bipolar transistor, an internal parasitic capacitance exists between a base and a collector. The capacitance value of the internal parasitic capacitance is increased due to the mirror effect. An internal resistance in the transistor, for example, a base parasitic resistance and the internal parasitic capacitance equivalently increased due to the mirror effect form a low-pass filter. The low-pass filter exerts adverse effects such as the reduction of the gain in the high frequency region.

The effect of the mirror effect in the amplifier shown in Fig. 18 will be described. Fig. 19 is an equivalent circuit diagram of the amplifier in a case where the transistor is represented by a hybrid  $\pi$ -type equivalent circuit. Fig. 20 is an equivalent circuit diagram of the amplifier in a case where consideration is given to the mirror effect.

In Figs. 19 and 20,  $r_b$  and  $r_\pi$  indicate internal parasitic resistances in the transistor 200, and  $C_\pi$  and  $C_c$  indicate internal parasitic capacitances in the transistor 200. The internal parasitic capacitance  $C_c$  between the base and the collector is a capacitance value which is multiplexed by  $(A_v+1)$  due to the mirror effect. Consequently, the frequency characteristics  $A_v(f)$  of the voltage gain in the amplifier is expressed by the following equation in an approximate manner:

$$A_v(f) = A_v / (1 + j\omega C_c r_\pi) \quad \cdots (6)$$

where  $f$  is a frequency, and  $\omega$  is an angular frequency. Further,  $C_T$  and  $r_T$  in the foregoing equation (6) are expressed by the following equation:

$$C_T = C\pi + C_c(1 + gmR_c) = C\pi + (1 + A_v) \cdot C_c$$

$$r_T = r_b \cdot r\pi / (r_b + r\pi)$$

where  $gm$  is a mutual inductance of the transistor 200.

Further, the frequency  $f_c$  which is lowered to 3dB ( $=1/\sqrt{2}$ ), as compared with the voltage gain  $A_v$  in the low frequency region, is expressed by the following equation (7):

$$f_c = 1 / (2\pi C_T r_T) \quad \dots (7)$$

The frequency characteristics of the wide-band amplifier is thus limited by the frequency  $f_c$ . When the foregoing equation (6) is rewritten using the foregoing equation (7), the following equation is obtained:

$$|A_v(f)| = A_v / \{1 + (f/f_c)^2\} \quad \dots (8)$$

From the foregoing equation (8), the voltage gain is reduced when the frequency is raised. When the reduction in the gain in the high frequency region is compensated for by adjusting the capacitance value  $C_c$  of the capacitor 18, the gain in the low frequency region is reduced.

On the other hand, a negative feedback amplifier comprises an amplification portion and a feedback portion. Generally, a resistor having no frequency dependency is used for the feedback portion, and the amplification portion is composed of a transistor or the like and has frequency

dependency. The gain  $G$  of the negative feedback amplifier is expressed by the following equation:

$$G = A / (1 - A \cdot \beta)$$

where  $A$  is open-loop gain (the gain of the amplification portion itself), and  $\beta$  is a feedback factor. When the open-loop gain  $A$  is reduced because the frequency is raised, the gain  $G$  of the whole negative feedback amplifier is reduced. In the wide-band amplifier or the like, therefore, the upper-limit frequency is limited.

10 In order to solve this, a method of decreasing a feedback amount as the frequency is increased by constituting the feedback portion by a capacitance, an inductance, and a resistance has been known. When such an inductive or capacitive element is used, however, the phase of a feedback  
15 signal varies depending on the frequency. Accordingly, the feedback portion enters a positive feedback state at a predetermined frequency, thereby causing problems. For example, the stability of the amplifier is degraded.

An object of the present invention is to provide a high  
20 frequency circuit capable of controlling characteristics in a high frequency region with a small occupied area and without degrading characteristics in a low frequency region.

#### SUMMARY OF THE INVENTION

An object of the present invention is to provide a high frequency circuit capable of controlling gain in a high frequency region without using a capacitor having a large occupied area and reducing gain in a low frequency region.

5 Still another object of the present invention is to provide a high frequency circuit whose frequency characteristics are improved without degrading stability.

A high frequency circuit according to an aspect of the present invention comprises a transistor having a first  
10 terminal receiving an input signal and having a second terminal and a third terminal; a first circuit connected to the second terminal of the transistor; and a second circuit connected to the third terminal of the transistor, at least one of the first and second circuits comprising one or a  
15 plurality of thin film resistors.

The thin film resistor has such characteristics that the film thickness thereof is relatively small, and the resistance value thereof is increased by a skin effect if the frequency thereof is increased. The skin effect is such a  
20 phenomenon that when a high frequency current flows through a conductor or a resistor, the high frequency current flows in such a manner as to be concentrated on its surface part. A skin depth is an index representing the skin effect. The skin depth  $\delta$  is expressed by the following equation:



$$\delta = \sqrt{\frac{1}{\pi \mu_0 \sigma f}} \quad \dots (9)$$

where  $\mu_0$  is permeability in a vacuum ( $=4\pi \times 10^{-7}$ ),  $\sigma$  is the conductivity of a material for the thin film resistor, and  $\rho$  is the resistivity of the material for the thin film resistor.

When the thin film resistor is composed of Au (gold), for example, the skin depth  $\delta$  at a frequency of 20 GHz is approximately 1  $\mu\text{m}$ . The resistance value of the thin film resistor at a particular frequency decreases as the film thickness thereof increases, and is almost saturated in a thickness which is approximately three times the skin depth. That is, if the film thickness of the thin film resistor is set to approximately the skin depth, a resistor whose resistance value varies depending on the frequency can be realized. A surface resistance in the thin film resistor is expressed by the following equation:

$$R_s(f) = \frac{\sqrt{\pi \mu_0 \sigma f} \times \rho}{1 - \exp(-t \sqrt{\pi \mu_0 \sigma f})} \quad \dots (10)$$

where  $t$  is the film thickness of the thin film resistor. From the foregoing equation (10), the surface resistance in the thin film resistor has frequency dependency using conductivity  $\sigma$  and a film thickness  $t$  which are peculiar to the material as parameters.

In the high frequency circuit according to the present invention, at least one of the first circuit connected to the second terminal of the transistor and the second circuit connected to the third terminal of the transistor comprises  
5 one or a plurality of thin film resistors. Consequently, the characteristics of at least one of the first and second circuits depend on the frequency. Consequently, it is possible to control the characteristics in the high frequency region with a small occupied area and without degrading the  
10 characteristics in the low frequency region.

The first circuit may be connected between the second terminal of the transistor and a power supply terminal receiving a power supply voltage, and the second circuit may be connected between the third terminal of the transistor and  
15 a grounding terminal receiving a grounding potential.

In this case, at least one of the first circuit connected between the second terminal of the transistor and the power supply terminal and the second circuit connected between the third terminal of the transistor and the grounding terminal  
20 comprises one or a plurality of thin film resistors. Consequently, the resistance value of at least one of the first and second circuits has frequency dependency. As a result, the gain of the transistor in the high frequency region depends on the frequency. Consequently, it is possible  
25 to control the gain in the high frequency region without using

a capacitor having a large occupied area and without reducing the gain in the low frequency region.

At least one of the first and second circuits may comprise a series connection of one or a plurality of thin film resistors and the one or a plurality of resistors having no frequency dependency.

In this case, it is possible to obtain arbitrary frequency characteristics by combining the thin film resistors and the resistors having no frequency dependency.

At least one of the first and second circuits may comprise a parallel connection of one or a plurality of thin film resistors and the one or a plurality of resistors having no frequency dependency.

In this case, it is possible to obtain arbitrary frequency characteristics by combining the thin film resistors and the resistors having no frequency dependency.

The first circuit may have a feedback circuit for feeding back a signal at the second terminal of the transistor to the first terminal, and the feedback circuit may comprise the one or plurality of thin film thin film resistors.

In this case, the feedback circuit for feeding back the signal at the second terminal of the transistor to the first terminal comprises one or a plurality of thin film resistors. Consequently, it is possible to control the frequency characteristics of the feedback factor of the feedback

circuit without changing the phase of a feedback signal of the feedback circuit. Consequently, it is possible to improve high frequency characteristics without degrading stability. The high frequency circuit may further comprise one or a plurality of other transistors, and the feedback circuit may feed back a signal at the second terminal to the first terminal through the one or plurality of other transistors.

In this case, in a feedback amplifier comprising transistors in a plurality of stages, it is possible to improve high frequency characteristics without degrading stability.

The feedback circuit may have a feedback path for applying a part of a voltage signal at the second terminal to the first terminal in series, and the feedback path may comprise the one or plurality of thin film resistors and one or plurality of resistors having no frequency dependency. In this case, in a voltage-series feedback amplifier, it is possible to improve high frequency characteristics without degrading stability.

The feedback circuit may have a feedback path comprising a conversion portion for converting a part of a current signal at the second terminal into a voltage signal and an application portion for applying the voltage signal obtained by the conversion portion to the first terminal in series, and the feedback path may comprise the one or plurality of

thin film resistors and one or plurality of resistors having frequency dependency.

In this case, in a current-series feedback amplifier, it is possible to improve high frequency characteristics without degrading stability.

The feedback circuit may have a feedback path comprising a conversion portion for converting a voltage signal at the second terminal into a current signal and an application portion for applying the current signal obtained by the conversion portion to the first terminal in parallel, and the feedback path may comprise the one or plurality of thin film resistors and one or plurality of resistors having no frequency dependency.

In this case, in a voltage-parallel feedback amplifier, it is possible to improve high frequency characteristics without degrading stability.

The feedback circuit may have a feedback path for applying a part of a current signal at the second terminal to the first terminal in parallel, and the feedback path may comprise the one or plurality of thin film resistors and one or plurality of resistors having no frequency dependency. In this case, in the voltage-parallel feedback amplifier, it is possible to improve high frequency characteristics without degrading stability.

The feedback circuit may have a first feedback path for applying a part of a voltage signal at the second terminal to the first terminal in series and a second feedback path for applying a part of a current signal at the second terminal to the first terminal in parallel, and each of the first feedback path and the second feedback path may comprise the one or plurality of thin film resistors and one or plurality of resistors having no frequency dependency.

In this case, in a feedback amplifier which is a combination of the voltage-series feedback amplifier and the current-parallel feedback amplifier, it is possible to improve high frequency characteristics without degrading stability.

The one or plurality of thin film resistors may have a film thickness smaller than three times its skin depth at a predetermined frequency. In this case, the resistance value of the feedback register depends on the frequency by the skin effect. Consequently, it is possible to control the gain at the predetermined frequency.

The one or plurality of thin film resistors may have a thickness of not more than the skin depth at the predetermined frequency. In this case, the resistance value of the feedback register has significant frequency dependency. In this case, it is possible to sufficiently control the gain at the predetermined frequency.

The transistor may be a bipolar transistor. Alternatively, the transistor may be a field effect transistor.

The one or plurality of thin film resistors may be formed  
5 of a thin film of a metal or a metal compound. The one or  
a plurality of resistors having no frequency dependency are  
composed of a semiconductor.

The foregoing and other objects, features, aspects and  
advantages of the present invention will become more apparent  
10 from the following detailed description of the present  
invention when taken in conjunction with the accompanying  
drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

15 Fig. 1 is a circuit diagram showing the configuration  
of an emitter grounded wide-band amplifier in a first  
embodiment of the present invention;

Fig. 2 is a diagram showing the frequency dependency of  
the resistance value of a thin film resistor used for the  
20 amplifier shown in Fig. 1;

Fig. 3 is a diagram showing the frequency  
characteristics of voltage gain in the amplifier shown in Fig.  
1;

Fig. 4 is a diagram showing the configuration of a first circuit in an emitter grounded wide-band amplifier in a second embodiment of the present invention;

Fig. 5 is a diagram showing the frequency characteristics of voltage gain in an amplifier in the second embodiment;

Fig. 6 is a circuit diagram showing an example of a first circuit of an emitter grounded wide-band amplifier in a third embodiment of the present invention;

Fig. 7 is a diagram showing the frequency characteristics of voltage gain in an amplifier in the third embodiment;

Fig. 8 is a diagram showing the principle of a negative feedback amplifier in a fourth embodiment of the present invention;

Fig. 9 is a diagram showing the frequency dependency of a surface resistance in a thin film resistor in a feedback portion;

Fig. 10 is a diagram showing the frequency dependency of the feedback factor of the feedback portion;

Fig. 11 is a diagram showing the frequency dependency of loop gain in the negative feedback amplifier shown in Fig. 8 using a feedback position having frequency dependency shown in Fig. 10 and a negative feedback amplifier using a feedback portion having a constant feedback factor;



Fig. 12 is a circuit diagram showing a voltage-series feedback amplifier in the present embodiment;

Fig. 13 is a circuit diagram showing a current-series feedback amplifier in the present invention;

5 Fig. 14 is a circuit diagram showing a voltage-parallel feedback amplifier in the present embodiment;

Fig. 15 is a circuit diagram showing a current-parallel feedback amplifier in the present embodiment;

10 Fig. 16 is a circuit diagram showing a composite feedback amplifier in the present embodiment;

Fig. 17 is a circuit diagram showing a voltage-series feedback amplifier in the present embodiment;

Fig. 18 is a circuit diagram showing the configuration of a conventional emitter grounded wide-band amplifier;

15 Fig. 19 is an equivalent circuit diagram of the amplifier shown in Fig. 18 in a case where a transistor is represented by a hybrid  $\pi$ -type equivalent circuit; and

Fig. 20 is an equivalent circuit diagram in a case where consideration is given to the mirror effect of the amplifier  
20 shown in Fig. 18.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

Description is now made of a wide-band amplifier as an example of a high frequency circuit according to the present  
25 invention.

## (1) First Embodiment

Fig. 1 is a circuit diagram of an emitter grounded wide-band amplifier in a first embodiment of the present invention.

5 The amplifier shown in Fig. 1 comprises an NPN-type bipolar transistor (hereinafter abbreviated as a transistor) 100, resistors 1 and 2, a first circuit 3, a second circuit 4, and capacitors 5 and 6. In the present embodiment, the first circuit 3 functions as a compensating circuit for  
10 compensating for the frequency characteristics of voltage gain.

The resistors 1 and 2 are formed by a semiconductor resistor or the like capable of almost ignoring frequency dependency. The first circuit 3 is constituted by a thin film  
15 resistor 30 having frequency dependency. Further, the second circuit 4 is constituted by a semiconductor resistor 40 capable of almost ignoring frequency dependency. The thin film resistor 30 is formed of a thin film of a metal or a metal compound, for example. Further, the semiconductor resistor  
20 40 is formed by ion-implanting impurities into a semiconductor, for example.

The transistor 100 has its base connected to a node N1, and the node N1 is connected to an input terminal NI through the capacitor 5. The resistor 1 is connected between a power  
25 supply terminal receiving a power supply voltage  $V_{cc}$  and the

node N1, and the resistor 2 is connected between the node N1 and a grounding terminal. The transistor 100 has its collector connected to the power supply terminal through the first circuit 3, and is connected to an output terminal NO through the capacitor 6. The transistor 100 has its emitter connected to the grounding terminal through the second circuit 4. An input signal is fed to the input terminal NI, and an amplified output signal is outputted from the output terminal NO.

In the amplifier shown in Fig. 1, a DC bias point of the transistor 100 is determined by the power supply voltage  $V_{CC}$ , the resistance value  $R_1$  of the resistor 1, the resistance value  $R_2$  of the resistor 2, the resistance value  $R_E$  of the semiconductor resistor 40 in the second circuit 4, and the resistance value  $R_{s0}$  in direct current of the thin film resistor 30 in the first circuit 3. The resistance value  $R_{s0}$  in direct current of the thin film resistor 30 is expressed by the following equation:

$$R_{s0} = L / (W \cdot t \cdot \sigma) \quad \cdots (11)$$

where  $\sigma$  is the conductivity of a material for the thin film resistor 30,  $L$  is the length of the thin film resistor 30,  $W$  is the width of the thin film resistor 30, and  $t$  is the film thickness of the thin film resistor 30. The resistance value  $R_{s0}$  in direct current of the thin film resistor 30 is given by the material for the thin film resistor 30

(conductivity  $\sigma$ ) and the shape thereof (length  $L$ , width  $W$ , and film thickness  $t$ ). A DC bias is determined using the resistance values  $R_1$ ,  $R_2$ ,  $R_E$ , and  $R_{S0}$ .

Furthermore, voltage gain  $A_v(f)$  in a high frequency region is expressed by the following equation (see IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES, VOL 37, NO. 10, OCTOBER 1989, pp. 241-247):

$$A_v(f) = \frac{RN_1(f)}{RN_2(f)} = \frac{R_S(f)}{R_E} \\ = \frac{\sqrt{\pi\mu_0\sigma f} \rho \left[ 1 - \exp\left(-t\sqrt{\pi\mu_0\sigma f}\right) \right]}{R_E} \times \frac{L}{W} \quad \dots(12)$$

where  $RN_1(f)$  is a composite resistance value of resistors constituting the first circuit 3, and  $RN_2(f)$  is a composite resistance value of resistors constituting the second circuit 4. In the present embodiment, a composite resistance value  $RN_1(f)$  in the first circuit 3 is the resistance value  $R_S(f)$  of the thin film resistor 30, and a composite resistance value  $RN_2(f)$  in the second circuit 4 is the resistance value  $R_E$  of the semiconductor resistor 40.  $f$  is a frequency,  $\mu_0$  is permeability ( $=4\pi \times 10^{-7}$ ) in a vacuum,  $\sigma$  is the conductivity of the material for the thin film resistor 30, and  $\rho$  is the resistivity of the material for the thin film resistor 30.

As can be seen from the foregoing equation (12), voltage gain  $A_v(f)$  in the amplifier depends on the material for the

thin film resistor 30 (conductivity  $\sigma$ ) and the shape thereof (length  $L$ , width  $W$ , and film thickness  $t$ ). Consequently, it is possible to obtain desired voltage gain  $A_v(f)$  by changing the material for the thin film resistor 30 and the shape thereof.

When the reduction in the voltage gain  $A_v(f)$  at a particular frequency is compensated for, the film thickness of the thin film resistor 30 is set to a thickness smaller than three times its skin depth at the frequency. Preferably, the film thickness of the thin film resistor 30 is set to not more than its skin depth at a frequency to be compensated for. Consequently, it is possible to compensate for the reduction in the voltage gain  $A_v(f)$  at the frequency.

Fig. 2 is a diagram showing an example of frequency dependency of the resistance value of the thin film resistor 30. In the example, a Ta (tantalum) based  $Ta_2N$  (tantalum nitride) superior in stability is used as the material for the thin film resistor 30. The resistivity  $\rho$  of  $Ta_2N$  is  $250 \times 10^{-6} \Omega \text{cm}$ . The film thickness  $t$  of the thin film resistor 30 is  $0.5 \mu\text{m}$ , the width  $W$  thereof is  $1 \mu\text{m}$ , and the length  $L$  thereof is  $2000 \mu\text{m}$ .

As shown in Fig. 2, the resistance value of the thin film resistor 30 is approximately  $10 \text{ k}\Omega$  at  $1 \text{ MHz}$ , is approximately  $11 \text{ k}\Omega$  at  $1 \text{ GHz}$ , and is approximately  $13.4 \text{ k}\Omega$  at  $10 \text{ GHz}$ .

Fig. 3 is a diagram showing the frequency characteristics of the voltage gain in the amplifier according to the first embodiment. In Fig. 3, a solid line indicates the frequency dependency of the voltage gain in the amplifier according to the present embodiment, and a broken line indicates the frequency dependency of the voltage gain in a case where the first circuit 3 which is constituted by the thin film resistor 30 in the amplifier shown in Fig. 1 is replaced with a normal resistor having no frequency dependency. A high frequency cut-off frequency  $f_c$  in this case is 5 GHz.

The resistance value  $R_s(f)$  of the thin film resistor 30 in the first circuit 3 has frequency dependency shown in Fig. 2. The resistance value  $R_s$  of the semiconductor resistor 40 in the second circuit 4 is taken as  $1\text{ k}\Omega$ .

As shown in Fig. 3, when the normal resistor is used between the collector of the transistor 100 shown in Fig. 1 and the power supply terminal, the voltage gain is reduced due to the mirror effect in the high frequency region of not less than 1 GHz. Contrary to this, when the first circuit 3 which is constituted by the thin film resistor 30 is used between the collector of the transistor 100 and the power supply terminal, the reduction in the gain in the high frequency region of not less than 1 GHz is improved.

In the amplifier according to the present embodiment, the first circuit 3 is constituted by the thin film resistor 30, thereby making it possible to control the frequency characteristics of the voltage gain without using a capacitor having a MIM structure having a large occupied area on an integrated circuit and having complicated processes and without reducing voltage gain in the low frequency region.

In this case, the frequency characteristics of the voltage gain are determined by the material for the thin film resistor 30 and the shape thereof. Consequently, it is possible to set desired voltage gain at a particular frequency by changing the material for the thin film resistor 30 and the shape thereof.

## (2) Second Embodiment

Description is now made of an emitter grounded wide-band amplifier in a second embodiment of the present invention. The amplifier in the second embodiment is the same as the amplifier shown in Fig. 1 except that a first circuit 3 comprises a series connection of one thin film resistor 31 and one semiconductor resistor 32, as shown in Fig. 4 (a). The semiconductor resistor 32 has little frequency dependency.

In the present embodiment, a composite resistance value  $R_{N1}(f)$  in the first circuit 3 in the foregoing equation (12) is expressed by the following equation:

$$RN_1(f) = R_s(f) + R \quad \cdots (13)$$

where  $R_s(f)$  is the resistance value of the thin film resistor 31, and  $R$  is the resistance value of the semiconductor resistor 32.

5        Fig. 5 is a diagram showing the frequency characteristics of voltage gain in the amplifier according to the second embodiment. In Fig. 5, a solid line indicates the frequency dependency of the voltage gain in the amplifier according to the present embodiment, and a broken line  
10 indicates the frequency dependency of the voltage gain in a case where the first circuit 3 in the amplifier shown in Fig. 1 is replaced with a normal resistor having no frequency dependency. A high frequency cut-off frequency in this case is 5 GHz.

15        A material for the thin film resistor 31 shall be  $Ta_2N$ . In this case, the resistivity  $\rho$  of  $Ta_2N$  is  $250 \times 10^{-6} \Omega \text{cm}$ . The film thickness  $t$  of the thin film resistor 31 is set to  $0.5 \mu\text{m}$ , the width  $W$  thereof is set to  $1 \mu\text{m}$ , and the length  $L$  thereof is set to  $1000 \mu\text{m}$ . The resistance value of the  
20 semiconductor resistor 32 is taken as  $5 \text{ k}\Omega$ . Further, the resistance value  $R_s$  of the semiconductor resistor 40 in the second circuit 4 is taken as  $1 \text{ k}\Omega$ .

When a first circuit 3 which is constituted by a series connection of the thin film resistor 31 and the semiconductor  
25 resistor 32 shown in Fig. 4 (a) is used between the collector



of the transistor 100 and the power supply terminal, as shown in Fig. 5, the reduction in the voltage gain in the high frequency region of not less than 1 GHz is improved, as compared with that in a case where the normal resistor is used.

5 In the amplifier according to the present embodiment, the first circuit 3 thus comprises the series connection of the thin film resistor 31 and the semiconductor resistor 32, thereby making it possible to control the frequency characteristics of the voltage gain without using a capacitor  
10 having an MIM structure having a large occupied area on an integrated circuit and having complicated processes and without reducing voltage gain in a low frequency region.

In this case, the frequency characteristics of the voltage gain are determined by the material for the thin film  
15 resistor 30 and the shape thereof. Consequently, it is possible to set desired voltage gain at a particular frequency by changing the material for the thin film resistor 30 and the shape thereof.

In the amplifier according to the present embodiment,  
20 the first circuit 3 may comprise a series connection of a plurality of thin film resistors 33 and 34, as shown in Fig. 4 (b). Further, in the amplifier according to the present embodiment, the first circuit 3 may comprise a series connection of a plurality of thin film resistors 35 and 36

and a plurality of semiconductor resistors 37 and 38, as shown in Fig. 4 (c).

Furthermore, the semiconductor resistor 32 shown in Fig. 4 (a) and the semiconductor resistors 37 and 38 shown in Fig. 4 (c) may be replaced with other resistors having little frequency dependency.

One or a plurality of thin film resistors and one or a plurality of resistors having little frequency dependency are arbitrarily connected in series, thereby making it possible to obtain various frequency characteristics in the amplifier.

### (3) Third Embodiment

Description is now made of an emitter grounded wide-band amplifier in a third embodiment of the present invention. The amplifier in the third embodiment is the same as the amplifier shown in Fig. 1 except that a first circuit 3 comprises a parallel connection of a thin film resistor 31 and a semiconductor resistor 32, as shown in Fig. 6 (a). The semiconductor resistor 32 has little frequency dependency.

In the present embodiment, a composite resistance value  $RN_1(f)$  in the first circuit 3 in the foregoing equation (12) is expressed by the following equation:

$$RN_1(f) = R_s(f) \times R / \{R_s(f) + R\} \quad \cdots (14)$$

where  $R_s(f)$  is the resistance value of the thin film resistor 31, and  $R$  is the resistance value of the semiconductor resistor 32.

Fig. 7 is a diagram showing the frequency characteristics of voltage gain in the amplifier according to the present embodiment. In Fig. 7, a solid line indicates the frequency dependency of the voltage gain in the amplifier according to the present embodiment, and a broken line indicates the frequency dependency of the voltage gain in a case where the first circuit 3 in the amplifier shown in Fig. 1 is replaced with a normal resistor having no frequency dependency. A high frequency cut-off frequency in this case is 5 GHz.

A material for the thin film resistor 31 shall be  $Ta_2N$ . In this case, the resistivity  $\rho$  of  $Ta_2N$  is  $250 \times 10^{-6} \Omega \text{cm}$ . The film thickness  $t$  of the thin film resistor 31 is set to  $0.5 \mu\text{m}$ , the width  $W$  thereof is set to  $1 \mu\text{m}$ , and the length  $L$  thereof is set to  $4000 \mu\text{m}$ . The resistance value  $R$  of the semiconductor resistor 32 is taken as  $20 \text{ k}\Omega$ . Further, the resistance value  $R_g$  of the semiconductor resistor 40 in the second circuit 4 is taken as  $1 \text{ k}\Omega$ .

When a first circuit 3 comprising a parallel connection of the thin film resistor 31 and the semiconductor resistor 32 is used between the collector of the transistor 100 and the power supply terminal, as shown in Fig. 7, the reduction in the voltage gain in the high frequency region of not less than 1 GHz is improved, as compared with that in a case where a normal resistor is used.

In the amplifier according to the present embodiment, the first circuit 3 thus comprises the parallel connection of the thin film resistor 31 and the semiconductor resistor 32, thereby making it possible to control the frequency characteristics of the voltage gain without using a capacitor having an MIM structure having a large occupied area on an integrated circuit and having complicated processes and without reducing voltage gain in a low frequency region.

In this case, the frequency characteristics of the voltage gain are determined by the material for the thin film resistor 30 and the shape thereof. Consequently, it is possible to set desired voltage gain at a particular frequency by changing the material for the thin film resistor 30 and the shape thereof.

In the amplifier according to the present embodiment, the first circuit 3 comprises a parallel connection of a plurality of thin film resistors 33 and 34, as shown in Fig. 6 (b). Further, in the amplifier according to the present embodiment, the first circuit 3 may comprise a parallel connection of a plurality of thin film resistors 35 and 36 and a plurality of semiconductor resistors 37 and 38, as shown in Fig. 6 (c).

Furthermore, the semiconductor resistor 32 shown in Fig. 6 (a) and the semiconductor resistors 37 and 38 shown

in Fig. 6 (c) may be replaced with other resistors having little frequency dependency.

One or a plurality of thin film resistors and one or a plurality of resistors having little frequency dependency are  
5 arbitrarily connected in parallel, thereby making it possible to obtain various frequency characteristics in the amplifier.

#### (4) Another Example of Configuration

Although in the second and third embodiments, the first circuit 3 comprises the series connection of the thin film  
10 resistor and the semiconductor resistor or the parallel connection of the thin film resistor and the semiconductor resistor, the configuration of the first circuit 3 is not limited to the configurations in the above-mentioned embodiments. For example, another circuit configuration  
15 comprising at least one thin film resistor may be used.

Although in the first, second and third embodiments, the first circuit 3 is constituted by at least one thin film resistor, the second circuit 4 may be constituted by one thin film resistor, similarly to the first circuit 3 in the first  
20 embodiment, may be constituted by a series connection of a thin film resistor and a semiconductor resistor, similarly to the first circuit in the second embodiment, or may be constituted by a parallel connection of a thin film resistor and a semiconductor resistor, similarly to the first circuit  
25 3 in the third embodiment.

Furthermore, the first circuit 3 or the fourth circuit 4 may be constituted by a combination of a series connection and a parallel connection of one or a plurality of thin film resistors and one or a plurality of semiconductor resistors.

5 Furthermore, both of the first circuit 3 and the second circuit 4 may be constituted by at least one thin film resistor.

For example, a resistance value between the collector of a transistor 100 and a power supply terminal at a frequency  
10  $f_c$  shown in the foregoing equation (7) is multiplexed by  $\sqrt{2}$  by the first circuit 3, or a resistance value between the emitter of the transistor 100 and a grounding terminal is multiplexed by  $1/\sqrt{2}$  by the second circuit 4, thereby making  
15 it possible to compensate for the reduction in voltage gain of 3 dB.

It is possible to realize an amplifier whose frequency characteristics are controlled in a complicated manner by arbitrarily combining one or a plurality of thin film resistors and one or a plurality of semiconductor resistors.

20 Although in the first, second, and third embodiments, the first circuit 3 is constituted by a thin film resistor, to compensate for the reduction in the voltage gain in the high frequency region, it is also possible to reduce voltage gain at a desired frequency by using the thin film resistor.

Furthermore, although in the first, second and third embodiments, description was made of a case where the present invention is applied to the emitter grounded wide-band amplifier, the present invention is not limited to the  
 5 amplifier. For example, the present invention is also applicable to various types of high frequency circuits. In this case, it is possible to control the frequency characteristics of the high frequency circuit.

#### (5) Fourth Embodiment

10 Description is now made of a negative feedback amplifier in a fourth embodiment of the present invention. Fig. 8 is a diagram showing the principle of the negative feedback amplifier in the fourth embodiment of the present invention.

As shown in Fig. 8, the negative feedback amplifier  
 15 comprises an amplification portion 101, a feedback portion 102, and an addition portion 103. Let  $A$  be open-loop gain, and let  $\beta$  be the feedback factor of the feedback portion 102. Let a cut-off frequency be  $f_c$ . Letting  $A_0$  be open-loop gain in direct current, and letting  $f$  be a signal frequency,  
 20 frequency dependency  $A(f)$  of the open-loop gain  $A$  is expressed by the following equation :

$$|A(f)| = \frac{A_0}{\sqrt{1 + (f/f_c)^2}} \quad \dots (15)$$

The foregoing equation (15) indicates that when the signal frequency  $f$  is equal to the cut-off frequency  $f_c$ , the open-loop gain  $A$  is  $\sqrt{1/2}$ .

Furthermore, loop gain (the gain of the feedback amplifier)  $G(f)$  is expressed by the following equation if consideration is given to frequency dependency:

$$G(f) = A(f) / \{1 - A(f) \cdot \beta(f)\} \quad \cdots (16)$$

In the conventional negative feedback amplifier, a resistor having no frequency dependency is used for a feedback portion, and the feedback factor is constant with respect to the frequency. Contrary to this, in the negative feedback amplifier in the present embodiment, a thin film resistor having frequency dependency is used for the feedback portion 102, resulting in improved frequency characteristics.

Typical examples of the feedback portion include a voltage feedback portion and a current feedback portion using two resistors, and the feedback factor thereof is expressed by the following equation:

$$\beta = R / (R + R_f) \approx R / R_f \quad \cdots (17)$$

$$R \ll R_f \quad \cdots (18)$$

where  $R$  and  $R_f$  are the resistance values of the two resistors. When a thin film resistor is used as the one resistor in the feedback portion 102, the feedback factor  $\beta(f)$  is expressed by the following equation, and has frequency



characteristics which depend on a material for the thin film resistor and the shape thereof:

$$\beta(f) = \frac{R}{R_f} = \frac{R}{R_s(f)} = \frac{R}{\frac{\rho}{\delta \left[ 1 - \exp\left(-\frac{t}{\delta}\right) \right]} \times \frac{L}{W}} \quad \dots(19)$$

In the foregoing equation (19),  $R_s(f)$  is a surface  
 5 resistance in the thin film resistor.  $\sigma$  is the conductivity  
 of the material for the thin film resistor, and  $\rho$  is the  
 resistivity of the material for the thin film resistor.  $L$   
 is the length of the thin film resistor,  $W$  is the width of  
 the thin film resistor, and  $t$  is the film thickness of the  
 10 thin film resistor. The feedback factor  $\beta(f)$  thus depends  
 on the material for the thin film resistor (conductivity  $\sigma$   
 and resistivity  $\rho$ ) and the shape thereof (length  $L$ , width  
 $W$ , and film thickness  $t$ ). By adjusting the material for the  
 thin film resistor and the shape thereof, therefore, a desired  
 15 feedback factor can be obtained.

Fig. 9 is a diagram showing the frequency dependency of  
 the surface resistance  $R_s(f)$  in the thin film resistor in the  
 feedback portion 102. In this example, Al (aluminum) is used  
 as the material for the thin film resistor. The resistivity  
 20  $\rho$  of Al is  $2.75 \times 10^{-6} \Omega \text{cm}$ . The film thickness  $t$  of the thin  
 film resistor is  $0.1 \mu\text{m}$ ,  $0.5 \mu\text{m}$ , and  $1.0 \mu\text{m}$ , the width  $W$   
 thereof is  $1 \mu\text{m}$ , and the length  $L$  thereof is  $200 \mu\text{m}$ .

As shown in Fig. 9, the larger the thickness of the metal film composing the thin film resistor is, the stronger the frequency dependency of the surface resistance  $R_s(f)$  is. Consequently, it is found that Al is suitable for a low resistance.

When a high resistance is required, a material having high resistivity  $\rho$ , for example, Ti ( $\rho = 43.1 \times 10^{-6} \Omega \text{cm}$ ) or  $\text{Ta}_2\text{N}$  ( $\rho = 250 \times 10^{-6} \Omega \text{cm}$ ).

Fig. 10 is a diagram showing frequency dependency of the feedback factor  $\beta$  of the feedback portion 102. In this example, Al is used as the material for the thin film resistor. The film thickness  $t$  of the thin film resistor is  $0.5 \mu\text{m}$ , the width  $W$  thereof is  $1 \mu\text{m}$ , and the length  $L$  thereof is  $200 \mu\text{m}$ . Further, the resistance value  $R$  of the resistor having no frequency dependency is  $10 \Omega$ .

As can be seen from Fig. 10, as the frequency increases, the feedback factor decreases.

Fig. 11 is a diagram showing the frequency dependency of loop gain  $G$  in the negative feedback amplifier shown in Fig. 8 using the feedback portion 102 having frequency dependency shown in Fig. 10 and a negative feedback amplifier using a feedback portion having a constant feedback factor. Open-loop gain is  $A=100$  (a cut-off frequency  $f_c=10$  [GHz]). A solid line indicates a case where the feedback portion 102 having the frequency dependency shown in Fig. 10 is used (the

thin film resistor is used), and a broken line indicates a case where the feedback factor is constant.

As can be seen from Fig. 11, when the feedback portion 102 having the frequency dependency shown in Fig. 10 is used, the frequency characteristics are improved in a high frequency region of not less than 5 GHz, as compared with that in a case where a feedback portion having a constant feedback factor is used.

#### (6) Specific Example of Negative Feedback Amplifier

A specific example of the negative feedback amplifier according to the present embodiment will be described.

Fig. 12 is a circuit diagram showing a voltage-series feedback amplifier in the present embodiment.

The voltage-series feedback amplifier shown in Fig. 12 comprises an FET (a field effect transistor) 301 in the front stage, an FET 302 in the back stage, a feedback resistor 311, a source resistor 312, drain resistors 313 and 314, capacitors C1 and C3, and an interstage capacitor C2.

The FET 301 has its gate connected to an input terminal NI through the capacitor C1, has its drain grounded through the drain resistor 313, and has its source grounded through the source resistor 312. The interstage capacitor C2 is connected between the drain of the FET 301 and the gate of the FET 302. The FET 302 has its source grounded, and has its drain connected to the source of the FET 301 through the

feedback resistor 311 and grounded through the drain resistor 314. The capacitor C3 is connected between the drain of the FET 302 and an output terminal NO. In Fig. 12, the illustration of a bias circuit is omitted.

- 5        The feedback resistor 311 and the source resistor 312 constitute a feedback portion. The feedback resistor 311 is formed of a thin film resistor having frequency dependency, and the source resistor 312 is formed of a semiconductor resistor capable of almost ignoring frequency dependency.
- 10    The feedback factor  $\beta$  of the feedback portion is expressed by the following equation:

$$\beta = -R_s / (R_s + R_f) \quad \cdots (20)$$

- where  $R_s$  is the resistance value of the source resistor 312, and  $R_f$  is the resistance value of the feedback resistor
- 15    311.

Open-loop gain A in the amplifier shown in Fig. 12 is expressed by the following equation:

$$A = g_m^2 R_{L1} R_{L2} / (1 + g_m R_s) \quad \cdots (21)$$

- where  $g_m$  is a mutual conductance,  $R_{L1}$  is the resistance
- 20    value of the drain resistor 313, and  $R_{L2}$  is the resistance value of the drain resistor 314.

- It is preferable that the resistance value  $R_f$  of the feedback resistor 311 is set to tens of ohms to hundreds of ohms, and the resistance value  $R_s$  of the source resistor 312
- 25    is set to several ohms to tens of ohms.

In the voltage-series feedback amplifier shown in Fig. 12, a thin film resistor is used as the feedback resistor 313 in the feedback portion, thereby improving frequency characteristics in a high frequency region.

5        Fig. 13 is a circuit diagram showing a current-series feedback amplifier in the present embodiment.

10        The current-series feedback amplifier shown in Fig. 13 comprises a bipolar transistor 304, a collector resistor 321, a feedback resistor 322, resistors 323 and 324, and capacitors C4 and C5.

15        The transistor 304 has its base connected to an input terminal NI through the capacitor C4, and has its collector connected to a power supply terminal VC through the collector resistor 321. In this case, the collector of the transistor 304 is grounded in a high frequency manner. The transistor 304 has its emitter grounded through the feedback resistor 322. The transistor 304 has its base connected to the power supply terminal VC through the resistor 323 and grounded through the resistor 324. The capacitor C5 is connected 20 between the collector of the transistor 304 and an output terminal NO.

25        The collector resistor 321 and the feedback resistor 322 constitute a feedback portion. The collector resistor 321 is formed of a thin film resistor having frequency dependency, and the feedback resistor 322 is formed of a semiconductor

resistor capable of almost ignoring frequency dependency. The feedback factor  $\beta$  of the feedback portion is expressed by the following equation:

$$\beta = -(1+h_{fe})R_f/h_{fe}R_c \approx -R_f/R_c \quad \cdots (22)$$

- 5        where  $h_{fe}$  is emitter grounded current gain,  $R_c$  is the resistance value of the collector resistor 321, and  $R_f$  is the resistance value of the feedback resistor 322.

Open-loop gain A in the amplifier shown in Fig. 13 is expressed by the following equation:

10         $A = h_{fe}/\{r_b + (1+h_{fe}) \cdot r_e\} \quad \cdots (23)$

where  $r_b$  is a base internal resistance in the transistor 304,  $r_e$  is an emitter internal resistance in the transistor 304.

- It is preferable that the resistance value  $R_c$  of the collector resistor 321 is set to hundreds of ohms to several kilo-ohms, and the resistance value  $R_f$  of the feedback resistor 322 is set to several ohms to hundreds of ohms.
- 15

- In the current-series feedback amplifier shown in Fig. 13, a thin film resistor is used as the collector resistor 321 in the feedback portion, thereby improving frequency characteristics in a high frequency region.
- 20

Fig. 14 is a circuit diagram showing a voltage-parallel feedback amplifier in the present embodiment.

The voltage-parallel feedback amplifier shown in Fig. 14 comprises a bipolar transistor 305, a feedback resistor 331, a collector resistor 332, and capacitors C6 and C7.

The transistor 305 has its base connected to an input terminal NI through the capacitor C6, and has its collector connected to a power supply terminal VC through the collector resistor 332. In this case, the collector of the transistor 305 is grounded in a high frequency manner. The transistor 305 has its emitter grounded. The feedback resistor 331 is connected between the collector and the base of the transistor 305. The capacitor C7 is connected between the collector of the transistor 305 and an output terminal NO.

The feedback resistor 331 and the collector resistor 332 constitute a feedback portion. The feedback resistor 331 is formed of a thin film resistor having frequency dependency, and the collector resistor 332 is formed of a semiconductor resistor capable of almost ignoring frequency dependency. The feedback factor  $\beta$  of the feedback portion is expressed by the following equation:

$$\beta = -R_c/R_f \quad \cdots (24)$$

where  $R_f$  is the resistance value of the feedback resistor 331, and  $R_c$  is the resistance value of the collector resistor 332.

Open-loop gain A in the amplifier shown in Fig. 14 is expressed by the following equation:

$$A = h_{fe} / \{1 + (1 + h_{fe}) \cdot R_C / R_f\} \quad \dots (25)$$

where  $h_{fe}$  is emitter grounded current gain. In this case, it is preferable that the resistance value  $R_f$  of the feedback resistor 311 is set to hundreds of ohms to tens of kilo-ohms, and the resistance value  $R_C$  of the collector resistor 322 is set to tens of ohms to several kilo-ohms.

In the voltage-series feedback amplifier shown in Fig. 14, a thin film resistor is used as the feedback resistor 331 in the feedback portion, thereby improving frequency characteristics in a high frequency region.

Fig. 15 is a circuit diagram showing a current-parallel feedback amplifier in the present embodiment.

The current-parallel feedback amplifier shown in Fig. 15 comprises a FET 306, a feedback resistor 341, a drain resistor 342, a gate resistor 343, a source resistor 344, and capacitors C8 and C9.

The FET 306 has its gate connected to an input terminal NI through the capacitor C8 and grounded through the gate resistor 343. The FET 306 has its drain connected to a power supply terminal VC through the drain resistor 342. In this case, the drain of the FET 306 is grounded in a high frequency manner. The FET 306 has its source grounded through the source resistor 344. The capacitor C9 is connected between the drain of the FET 306 and an output terminal NO. Further,



the feedback resistor 341 is connected between the output terminal NO and the gate of the FET 306.

The feedback resistor 341 and a signal source impedance  $R_{in}$  constitute a feedback portion. The feedback resistor 341 is formed of a thin film resistor having frequency dependency, and the feedback factor  $\beta$  of the feedback portion is expressed by the following equation:

$$\beta = R_{in}/R_f \quad \dots (26)$$

where  $R_f$  is the resistance value of the feedback resistor 341, and  $R_{in}$  is the signal source impedance.

Open-loop gain A in the amplifier shown in Fig. 15 is expressed by the following equation:

$$A = g_m R_L / (r_d + R_L) \quad \dots (27)$$

where  $g_m$  is a mutual conductance,  $R_L$  is the resistance value of the drain resistor 342, and  $r_d$  is a drain internal resistance. In this case, when the signal source impedance  $R_{in}$  is taken as  $50\Omega$ , it is preferable that the resistance value  $R_f$  of the feedback resistor 341 is set to hundreds of ohms to several kilo-ohms.

In the current-series feedback amplifier shown in Fig. 15, a thin film resistor is used as the feedback resistor 341 in the feedback portion, thereby improving frequency characteristics in a high frequency region.

Fig. 16 is a circuit diagram showing a composite feedback amplifier in the present embodiment. The composite

feedback amplifier shown in Fig. 16 is a combination of the voltage-series feedback amplifier and the current-parallel feedback amplifier.

The composite feedback amplifier shown in Fig. 16  
 5 comprises a bipolar transistor 307 in the front stage, a bipolar transistor 308 in the back stage, feedback resistors 351 and 352, emitter resistors 353 and 354, an output resistor 355, and capacitors C10 and C11.

The transistor 307 in the front stage has its base  
 10 connected to an input terminal NI through the capacitor C10, has its collector connected to the base of the transistor 308 in the back stage, and has its emitter grounded through the emitter resistor 353. The transistor 308 in the back stage has its collector connected to the emitter of the transistor  
 15 307 in the front stage through the feedback resistor 351. The transistor 308 in the back stage has its emitter grounded through the emitter resistor 354 and connected to the base of the transistor 307 in the front stage through the feedback resistor 352. The capacitor C11 is connected between the  
 20 collector of the transistor 308 and an output terminal NO, and the output terminal NO is grounded through the output resistor 355. In Fig. 16, the illustration of a bias circuit is omitted.

The feedback resistor 351 and the emitter resistor 353  
 25 constitute a first feedback portion, and the feedback

resistor 352 and a signal power impedance constitute a second feedback portion. The feedback resistors 351 and 352 are formed of a thin film resistor having frequency dependency, and the emitter resistor 353 is formed of a semiconductor resistor capable of almost ignoring frequency dependency.

In the composite feedback amplifier shown in Fig. 16, thin film resistors are respectively used as the feedback resistors 351 and 352, thereby improving frequency characteristics in a high frequency region.

Fig. 17 is a circuit diagram showing a voltage-series feedback amplifier in the present embodiment.

The voltage-series feedback amplifier shown in Fig. 17 comprises FETs 309, 310, and 311 in three stages, a feedback resistor 361, a source resistor 362, drain resistors 363, 364, and 365, capacitors C12 and C15, and interstage capacitors C13 and C14.

The FET 309 has its gate 7 connected to an input terminal NI through the capacitor C12, has its source grounded through the source resistor 362, and has its drain grounded through the drain resistor 363. The FET 310 has its gate connected to the drain of the FET 309 through the interstage capacitor C13, has its source grounded, and has its drain grounded through the drain resistor 364. The FET 311 has its gate connected to the drain of the FET 310 through the interstage capacitor C14, has its source grounded, and has its drain

connected to the source of the FET 309 through the feedback resistor 361 and grounded through the drain resistor 365. The capacitor C15 is connected between the drain of the FET 311 and an output terminal NO. In Fig. 17, the illustration of a bias circuit is omitted.

The feedback resistor 361 and the source resistor 362 constitute a first feedback portion. The feedback resistor 361 is formed of a thin film resistor having frequency dependency, and the source resistor 362 is formed of a semiconductor resistor capable of almost ignoring frequency dependency.

In the voltage-series feedback amplifier shown in Fig. 17, frequency characteristics in a high frequency region are improved by using a thin film resistor as the feedback resistor 361.

Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.